

Logic Circuit Using Hardware to Process Keyboard Scanning

BACKGROUND OF THE INVENTION

1) FIELD OF THE INVENTION

5 The present invention relates to a logic circuit using hardware to process keyboard scanning, more especially to a microprocessing system applied to a computer, a mobile phone, an electronic dictionary and calculator using a pressed key to input the signal into a control circuit for conducting operation for an efficient detecting method of recognizing the locations and numbers of pressed keys.

2) DESCRIPTION OF THE PRIOR ART

10 The common and familiar method of keyboard recognition mainly has a diode disposed between an input and an output ports; the unidirectional current conduction of the diode detects whether a current is passing by for recognizing a valid signal of a presses key.

15 However, the abovementioned method for detecting whether there is a pressed key signal has to be added with one to a plurality of current conducting diode and that complicates the circuit detection; furthermore, the increased components also increase unnecessary power consumption; the degree of wearing of the microprocessing system in a computer, a mobile phone, an electronic dictionary and calculator using a
20 pressed key to input the signal into a control circuit for conducting operation will also

be increased.

SUMMARY OF THE INVENTION

Therefore, the objective of the present invention is to save cost and be capable of reducing power consumption, which does not inconvenience the application due to the augmented function of reducing power consumption, but increases and enhances the consistent operation of a computer, a mobile phone, an electronic dictionary and calculator due to the unique method of the present invention, thereby eliminates the shortcomings of being inconvenient for an user to use and increases the practicality and convenience thereof.

To enable a further understanding of the method, the procedure, the spirit and the other objectives of the present invention, the brief description of the drawings below is followed by the detailed description of the preferred embodiment.

BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 is an operating flow chart of the present invention.

Figure 2 is a drawing of the logic circuit of the present invention.

Figure 3 is a drawing of the logic circuit and the oscillogram of the first exemplary embodiment of the present invention.

Figure 4 is a drawing of the logic circuit and the oscillogram of the second exemplary embodiment of the present invention.

Figure 5 is a drawing of the logic circuit and the oscillogram of the third

exemplary embodiment of the present invention.

Figure 6 is a drawing of the logic circuit and the oscillogram of the fourth exemplary embodiment of the present invention.

Figure 7 is a drawing of the logic circuit and the oscillogram of the fifth
5 exemplary embodiment of the present invention.

Figure 8 is a drawing of the logic circuit and the oscillogram of the sixth exemplary embodiment of the present invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Referring to FIGS. 1 and 2, mainly, the present invention of a logic circuit
10 using hardware to process keyboard scanning utilizes the driving ability of electric resistances (R_a to R_e) of column input ends (IA to IE) higher than that connected to electric resistances (R_{oa} to R_{oe}) of row output/input ends (IOA to IOE) to accordingly dispose the column input ends (IA to IE) formed naturally at the higher electrical potential status and the row output/input ends (IOA to IOE); when the key
15 located at the crossing point of the column input end and the row output/input end is pressed, the current of the said column input end and the row output/input end conduct with each other for changing the high and lower electrical potential status to approach to a consistent and equal electric potential.

When one or a plurality of specially appointed keys are pressed to generate
20 output/input end driving logic, the electrical potentials are conducted with each other;

after conduction, one of the row output/input ends (IOA to IOE) should convert from a low electric potential status to a high electric potential status; when a keyboard matrix detects that any one or more than one signal of the row output/input ends (IOA to IOE) convert from a lower electric potential status to a higher electric potential status, that means a key is pressed.

A pressed key detecting logic alternately detects that whether one of the row output/input ends (IOA to IOE) with elevated electric potential changes to a low electric potential status; since the pressed key conducts the electrical potential between the said column and row to an equal state, when one or more than one of the column input ends (IA to IE) converts from a high electric potential to a low electric state, the location of the pressed key can be detected through the crossing point of the said matrix; then the pressed key detecting logic informs an external logic to store this one or a plurality of column and row numbers in a memory for storing pressed key locations.

After the external logic stores the column and row numbers in the memory for storing pressed key locations, the pressed key detecting logic waits for a period of time of unstable signals and then inputs the signals; it is required that only if the same result is obtained after two detections, then the pressed key is listed as a valid one and a report is output to the system for execution.

Furthermore, no matter whether a key is pressed or not, the pressed key

detecting logic can always sequentially and alternately drive one of the row output/input ends (IOA to IOE) to a low electric potential status; when one of the row output/input ends (IOA to IOE) is driven to a low electric potential status and the pressed key logic detects that all of the column input ends (IA to IE) are in a high electric potential status, that means this column does not have any pressed key, then the pressed key detecting logic moves to drive the next row output/input signal.

When one of the row output/input ends (IOA to IOE) is driven to a low electric potential status and the pressed key detecting logic detects any one or more than one of the column input ends (IA to IE) is in a low electric status, it informs the external logic to store this one or a plurality of column and row numbers in the memory for storing pressed key locations.

After the external logic stores the column and row numbers in the memory for storing pressed key locations, the pressed key detecting logic waits for a period of time of unstable signals and then inputs the signals; it is required that only if the same result is obtained after two detections, then the pressed key is listed as a valid one and a report is output to the system for execution.

Referring from FIG. 3 to FIG. 8, the exemplary embodiment of the present invention using hardware to process keyboard scanning, the first example (as shown in FIG. 3) indicates that when key B is pressed, (IOB) is then driven to a high electric status because of the electric potential conduction; when the pressed key detecting

logic drives the row (IOB) to a low electric potential status, the column (IB) is also driven to a low electric potential; therefore, the pressed key detecting logic determines that the column and row location of key B is IOB and IB; it also stores this data in the memory for storing pressed key locations.

5 The second example (as shown in FIG. 4), when keys B and A are pressed synchronously, the row (IOB) is driven to a high electrical potential status; when the pressed key detecting logic drives the row (IOB) to a low electric potential status, the column (IB) and the column (IC) are also driven to a low electric potential; therefore, the pressed key detecting logic determines that both of the keys B and A are pressed
10 and the column and row location thereof is (IOB) and (IB) as well as (IOB) and (IC), it also stores this data in the memory for storing pressed key locations.

 The third example (as shown in FIG. 5), when key B and C are pressed at the same time, the rows (IOB) and (IOC) are driven to a high electric potential status; when the pressed key detecting logic drives the row (IOB) to a low electric potential
15 status, column (IB) is driven to a low electric potential status, however, at the same time, the column (IOC) is also driven to a low electric potential status; this influence will be discussed in the fifth (as shown in FIG. 7) and the sixth (as shown in FIG. 8) examples; in this example, the data about the row (IOC) is also driven to a low electric potential status can be ignored; therefore, the pressed key detecting logic
20 determines that key B is pressed and the column and row location thereof is (IOB)

and (IB) to be stored in the memory for storing pressed key locations; sequentially, when the pressed key detecting logic drives the row (IOB) to a high electric potential status and the row (IOC) to a low electric potential status, the column (IB) is driven to a low electric potential status, however, the row (IOB) is also driven to a low electric potential status; as the same, the pressed key detecting logic determines that key C is pressed and the column and row location thereof is (IOC) and (IB) to be stored in the memory for storing pressed key locations.

The fourth example (as shown in FIG. 6), when keys B and D are pressed synchronously, the rows (IOB) and (IOC) are driven to a high electric potential status; when the pressed key detecting logic drives the row (IOB) to a low electric potential status, the column (IB) is driven to a low electric potential status; therefore, the pressed key detecting logic determines that key B is pressed and the column and row location thereof is (IOB) and (IB) to be stored in the memory for storing pressed key locations; sequentially, when the pressed key detecting logic drives the row (IOB) to a high electric potential status and the row (IOC) to a low electric potential status, the column (IC) is driven to a low electric potential status; therefore, the pressed key detecting logic determines that key D is pressed and the column and row location thereof is (IOC) and (IC) to be stored in the memory for storing pressed key locations.

The fifth example (as shown in FIG. 7), when keys A, B and D are pressed synchronously, the rows (IOB) and (IOC) are driven to a high electric potential status;

when the pressed key detecting logic drives the row (IOB) to a low electric potential status, the column (IB) and column (IC) are driven to a low electric potential status; however, at the same time, the row (IOC) is also driven to a low electric potential status; therefore, the key C is defined as an uncertain key.

5 Referring to FIG. 8 for the sixth example, when keys A, B, C and D are pressed synchronously, the same situation will happen; however, the pressed key detecting logic can use the following two conditions to detect the existence of an uncertain key:

1. When more than one electric potential of the row output/input ends
10 (IOA to IOE) are in a low electric potential status (including the row output/input terminals driven by the pressed key detecting logic).
2. When more than one electric potential of the column input ends (IA to IE) is in a low electric potential status.

The abovementioned two conditions must exist at the same time to thereby
15 determine that the state of having an uncertain key exists.

In summation of the abovementioned, the present invention provides a logic circuit using hardware to process keyboard scanning for detecting and recognizing a pressed key; the present invention not only eliminates the shortcomings of the prior art of detecting by means of complicate and expensive components, but also possesses
20 the effect of lowest power consumption; it is based on innovative ideas and spirit;

furthermore, it has not been preceded in any previous patent application and is not available on the market.

It is of course to be understood that the embodiment described herein is merely illustrative of the principles of the invention and that a wide variety of
5 modifications thereto may be effected by persons skilled in the art without departing from the spirit and scope of the invention as set forth in the following claims.